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I hereby certify that the attached Transmittal (2 pages), Response to the Office Action dated November 17, 2004 (8 pages), are submitted to the U.S. Patent and Trademark Office via facsimile number 703-872-9306 on the date shown below. (Total 10 pages).

  
Li Mei Vernilya

Date: January 31, 2005

PATENT APPLICATION  
Docket No.: 9898-293  
Client Ref. No.: SS-17762-US

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of: Hoe-Ju CHUNG and Kyu-Hyoun KIM

Serial No.: 10/656,303 Examiner: Auduong, Gene Nghia

Filed: September 4, 2003 Group Art Unit: 2818

Confirmation No.: 8209

For: SEMICONDUCTOR MEMORY DEVICE HAVING DUTY CYCLE  
CORRECTION CIRCUIT AND INTERPOLATION CIRCUIT  
INTERPOLATING CLOCK SIGNAL IN THE SEMICONDUCTOR  
MEMORY DEVICE

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Responsive to the Office Action dated November 17, 2004, enclosed is an amendment in the above-identified application.

The fee has been calculated as shown below.

CLAIMS AS AMENDED					
For:	Number After Amendment	Previous Number	Extra	Rate	Additional Fee
Total Claims	15	15	0	x \$50 =	\$0
Independent Claims	4	4	0	x \$200 =	\$0
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT					\$0

\*greater of twenty (20) or number for which fee has been paid

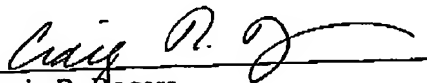
\*\*greater of three (3) or number for which fee has been paid

☒ Any deficiency or overpayment should be charged or credited to deposit account number 13-1703.

Customer No. 20575

Respectfully submitted,

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CORRECTION CIRCUIT AND INTERPOLATION CIRCUIT  
INTERPOLATING CLOCK SIGNAL IN THE SEMICONDUCTOR  
MEMORY DEVICEMail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

## AMENDMENT

Responsive to the Office Action, Paper No. 110304, dated November 17, 2004, please  
amend the application as follows.

**Amendments to the Claims** are reflected in the listing of claims which begins on  
page 2 of this paper.

**Remarks/Arguments** begin on page 7 of this paper.